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Chameleon (The Very Hungry  
Caterpillar \u0026amp; Other  
Stories)~~

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Toward a Culture of

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Simulation and Debug of  
Mixed Signal Virtual  
Platforms for Hardware-  
Software co-development.

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Systems, Inc.

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Abstract-Virtual platforms  
are often used for high  
level architecture  
exploration and hardware-  
software interactions  
verification.

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Simulink ® supports  
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Virtual Platforms For  
back and forth through your simulation. Use the Simulation Stepper to view data and inspect how and when the system changes states. For information, see How Simulation Stepper Helps With Model Analysis..

Simulink also includes the Simulink Debugger, which, like the Simulation Stepper, also enables you to start, stop ...

~~Test and Debug Simulations~~

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when the system changes  
states. For information, see  
How Simulation Stepper Helps  
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Mixed reach the required  
simulation speed. The  
Cadence Incisive Enterprise  
Simulator supports all these  
languages and is also able  
to provide debugger and  
profiler to understand,  
debug and optimize the  
performance of the mixed



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simulation. Based on this,  
we present the experiments  
made with a virtual platform

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Synopsys' Verdi® Advanced  
AMS Debug provides  
comprehensive views of the  
overall design and enables  
seamless debug for co-  
simulation of analog,  
digital and mixed-signal  
subsystems within a unified  
debug environment.

~~Verdi Advanced AMS Debug~~  
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For simulation of  
continuous, discrete, and  
mixed-signal systems, you  
can choose from a range of  
fixed-step and variable-step  
solvers. Solvers are  
integration algorithms that  
compute system dynamics over  
time. ... View and Analyze  
Simulation Results View

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Vivado® Simulator results to  
prototype and debug models,  
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Simulink — MathWorks~~ 

Vivado® Simulator is a  
feature-rich, mixed-language  
simulator that supports  
Verilog, SystemVerilog and  
VHDL language. Vivado  
Simulator is included in all  
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have a design size,  
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and it allows to run  
unlimited instances of mixed-  
language simulation using  
single ...

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Simulink also supports debugging with the Simulation Stepper, which lets you step back and forth through your simulation, viewing data and inspecting how and when the system changes states. For information, see How Simulation Stepper Helps With Model Analysis .

~~Simulink Debugger — MATLAB & Simulink~~

For simulation of continuous, discrete, and mixed-signal systems, you can choose from a range of fixed-step and variable-step solvers. Solvers are integration algorithms that compute system dynamics over

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time. ... View and Analyze  
Simulation Results View  
simulation results to  
prototype and debug models,  
...

~~Simulation — MATLAB &  
Simulink — MathWorks United  
Kingdom~~

SimVision Debug can be used  
to debug digital, analog, or  
mixed-signal designs written  
in Verilog, SystemVerilog,  
e, VHDL, and SystemC ®  
languages or a combination  
thereof. SimVision  
integrated debug supports  
signal-level and transaction-  
based flows across all IEEE-  
standard design, testbench,  
and assertion languages, in  
addition to concurrent

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Visualization of hardware,  
software, and analog  
domains.

~~SimVision Debug — Cadence~~  
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Almotive has adopted  
Synopsys VCS simulation and  
Verdi debug, part of the  
Verification Continuum  
Platform, to help verify its  
innovative aiWare hardware  
IP for Neural Network (NN)  
acceleration for automated  
driving applications.

~~Synopsys VCS simulation to  
verify automated  
technologies~~  
Simulate mixed-signal  
designs with the Spectre AMS  
Designer/Xcelium mixed-

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SystemVerilog for Design,  
VHDL, and SystemC provide a  
solid foundation for single  
and multi-language design  
verification environments.

~~ModelSim ASIC and FPGA  
Design - Mentor Graphics~~

The initialization, running,  
debugging and maintenance of  
these systems are extremely  
difficult tasks that, if not  
carefully performed, can use  
up the resource savings that  
mixed-reality simulations  
are designed to achieve.  
These issues are addressed  
through a set of computer  
applications known as the  
Mixed Reality Toolbox - or,  
MRT.



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Simulation | Virtual Reality~~

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Multi-Core simulation which supports all design languages and constructs and either automatically or manually partitions the design to run in parallel while maintaining a single database for debug and coverage.

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